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Applicant(s): Efren M. Lacap

408204

Serial No.

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Examiner

Group Art Unit

10/648,586

August 26, 2003

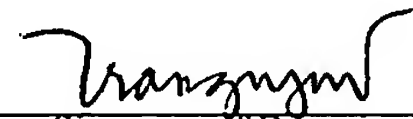
Jonathan J. Johnson

1725

Invention      Wafer-Level Chip Scale Packaging

I hereby certify that this Transmittal Form (1 page); Amendment And Response to Restriction Requirement (11 pages); Replacement Drawings Figs. 1-14 (14 pages), and return post card are being mailed in an envelope addressed to: Mail Stop: Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 25th day of April, 2005.

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EV509127603US  
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**TRANSMITTAL  
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

27

Application Number

10/648,586

Filing Date

August 26, 2003

First Named Inventor

Efren M. Lacap

Art Unit

1725

Examiner Name

Jonathan J. Johnson

Attorney Docket Number

408204

**ENCLOSURES (check all that apply)**☐ Fee Transmittal Form☐ Fee Attached☒ Amendment / Reply☐ After Final☐ Affidavits/declaration(s)☐ Extension of Time Request☐ Express Abandonment Request☐ Information Disclosure Statement☐ Certified Copy of Priority Document(s)☐ Reply to Missing Parts/  
Incomplete Application☐ Reply to Missing Parts  
under 37 CFR1.52 or 1.53☒ Drawing(s) -- Replacement☐ Licensing-related Papers☐ Petition☐ Petition to Convert to a  
Provisional Application☐ Power of Attorney, Revocation  
Change of Correspondence Address☐ Terminal Disclaimer☐ Request for Refund☐ CD, Number of CD(s) \_\_\_\_\_☐ Landscape Table on CD☐ After Allowance Communication to TC☐ Appeal Communication to Board  
of Appeals and Interferences☐ Appeal Communication to TC  
(Appeal Notice, Brief, Reply Brief)☐ Proprietary Information☐ Status Letter☒ Other Enclosure(s)  
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LATHROP &amp; GAGE LC

Signature

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Heather F. Perrin

Date

April 25, 2005

Reg.  
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52,884

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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PATENT  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)	Efren M. Lacap	Examiner	Jonathan J. Johnson
Serial No.	10/648,586	TC/AU No.	1725
Filed	26 August 2003	Confirmation No.	4089
For	WAFER-LEVEL CHIP SCALE PACKAGING		

Mail Stop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT

Sir:

These remarks are responsive to the Office Action of 24 March 2005. Please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper

**Amendments to the Drawings** begin on page 4 of this paper

**Amendments to the Claims** begin on page 5 of this paper.

**Remarks/Arguments** begin on page 8 of this paper.